A USER NON-VOLATILE MEMORY INTERFACE MEGAFUNCTION

Abstract of the Disclosure

[0080]   An interface for a programmable logic device
having a non-volatile memory where a portion of the non-
volatile memory is user accessible is provided.   A
5   megafunction provides the electronic circuit designer with
interface protocol options for the user accessible portion
of the non-volatile memory block. The circuitry associated
with the user selected interface is then programmed into
the programmable logic device.